

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

H. Ikeda et al.

Group Art Unit: 2818

Serial No.: 09/917,913

Examiner: C. Yoha

Filed: July 31, 2001

Docket No. 100353-00065

For: SEMICONDUCTOR MEMORY DEVICE HAVING AN SRAM AND A DRAM ON A  
SINGLE CHIP**RESPONSE TO RESTRICTION REQUIREMENT**

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Commissioner for Patents  
Washington, D.C. 20231

July 1, 2002

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TECHNOLOGY CENTER 2800

Sir:

In response to the telephone Election/Restriction Requirement made by Examiner Yoha on June 18, 2002, the Applicants hereby elect Group II, directed to claims 8-10 and 13-14, drawn to the control unit for controlling source voltages.

The Applicants reserve the right to file one or more divisional applications on the non-elected invention.

In view of the Applicants' above election, Applicants respectfully submit that the Restriction Requirement has been satisfied. Accordingly, Applicants respectfully request examination of elected claims 8-10 and 13-14 on the merits.

Please charge any fee deficiency or credit any overpayment to Deposit Account No. 01-2300, referring to client-matter number 107348-00032.

Respectfully submitted,

Bala Sundararajan  
Attorney for Applicants  
Reg. No. P 50,900ARENT FOX KINTNER PLOTKIN & KAHN PLLC  
1050 Connecticut Avenue, N.W., Suite 400  
Washington, D.C. 20036-5339  
Tel (202) 857-6000  
Fax (202) 638-4810  
BKS/bgk